

FIG 1

USB Peripheral Controller

Logic Block Diagram

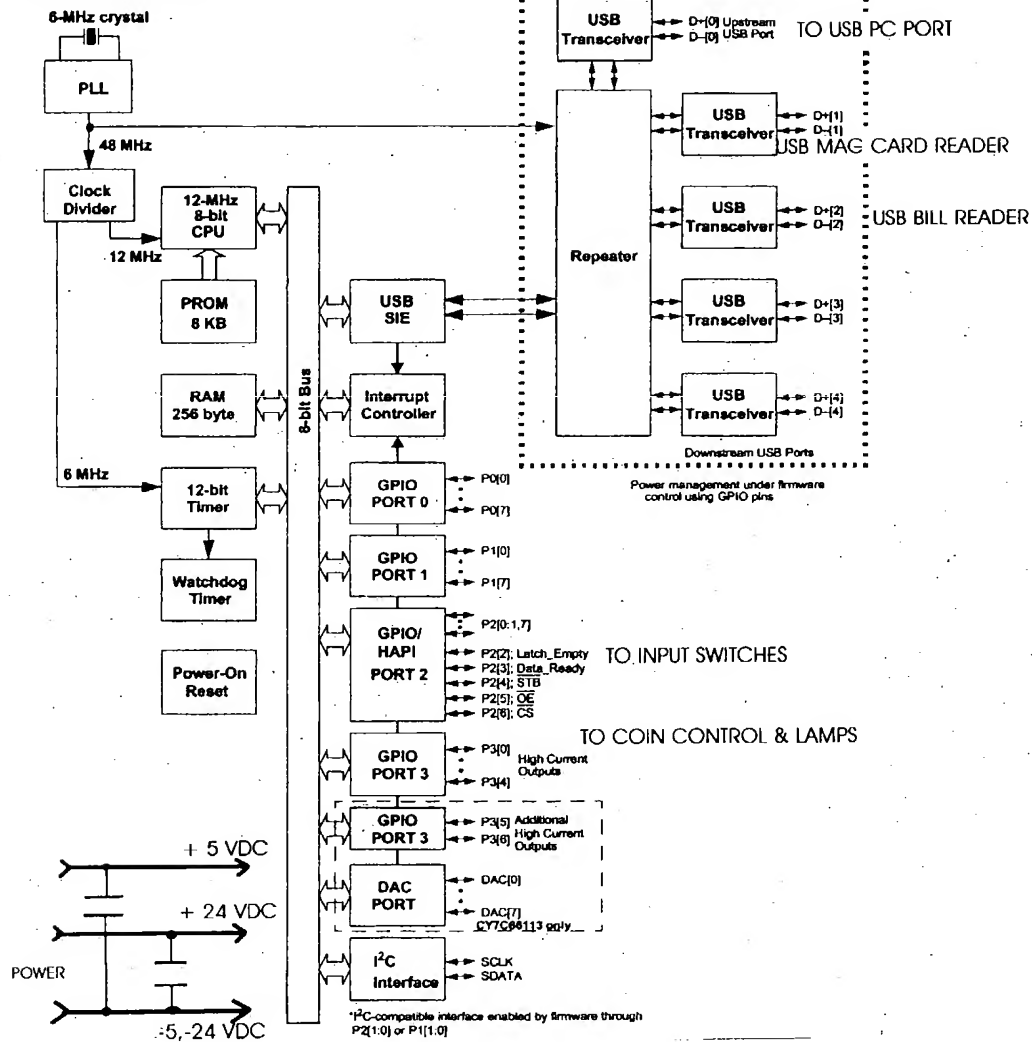


FIG 2

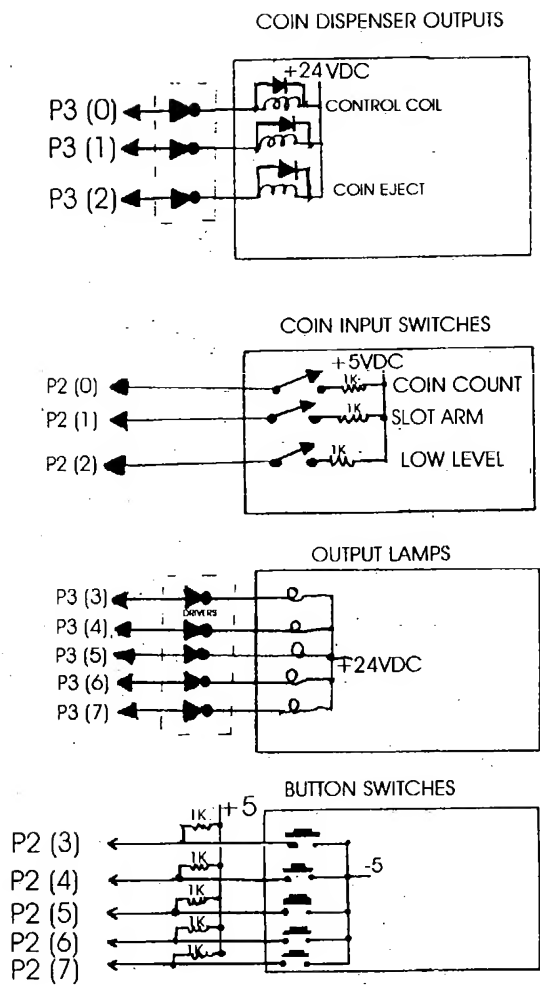


FIG 3